



AF
2822

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

Application of:
Li et al.

Fee Only

Group Art Unit: 2822
Examiner: Lewis, M.

Serial No. 09/849,047

Atty. Dkt. No. 5298-04500
(PM00026)

Filed: May 4, 2001

For: BURIED LAYER SUBSTRATE
ISOLATION IN INTEGRATED
CIRCUITS

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Kevin L. Daffer

APPEAL BRIEF

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Commissioner for Patents
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Sir/Madam:

Further to the Notice of Appeal sent via facsimile on February 4, 2004, the Appellant presents this Appeal Brief. The Notice of Appeal was filed following receipt of an Office Action mailed November 5, 2003. The Appellant hereby appeals to the Board of Patent Appeals and Interferences rejections of claims 1-13, 17-21, and 23-25 and respectfully requests that this appeal be considered by the Board.

I. REAL PARTY IN INTEREST

The subject application is owned by Cypress Semiconductor Corporation, a corporation having a place of business at 3901 North First Street, San Jose, CA, 95134.

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